

PRODUCT	TFT LCD MODULE
MODEL NO.	FT3563JPY-20P
DATE	2020-4-29

SPECIFICATION

产品规格书

Version: V0

This module uses ROHS material
模块用环保材料

CUSTOMER (客户):

Customer Approval(客户核准)

Fangsheng (DG) Electronic Co., Ltd:

Technical Department 工程部		Quality Department 品质部	Approved by 核准
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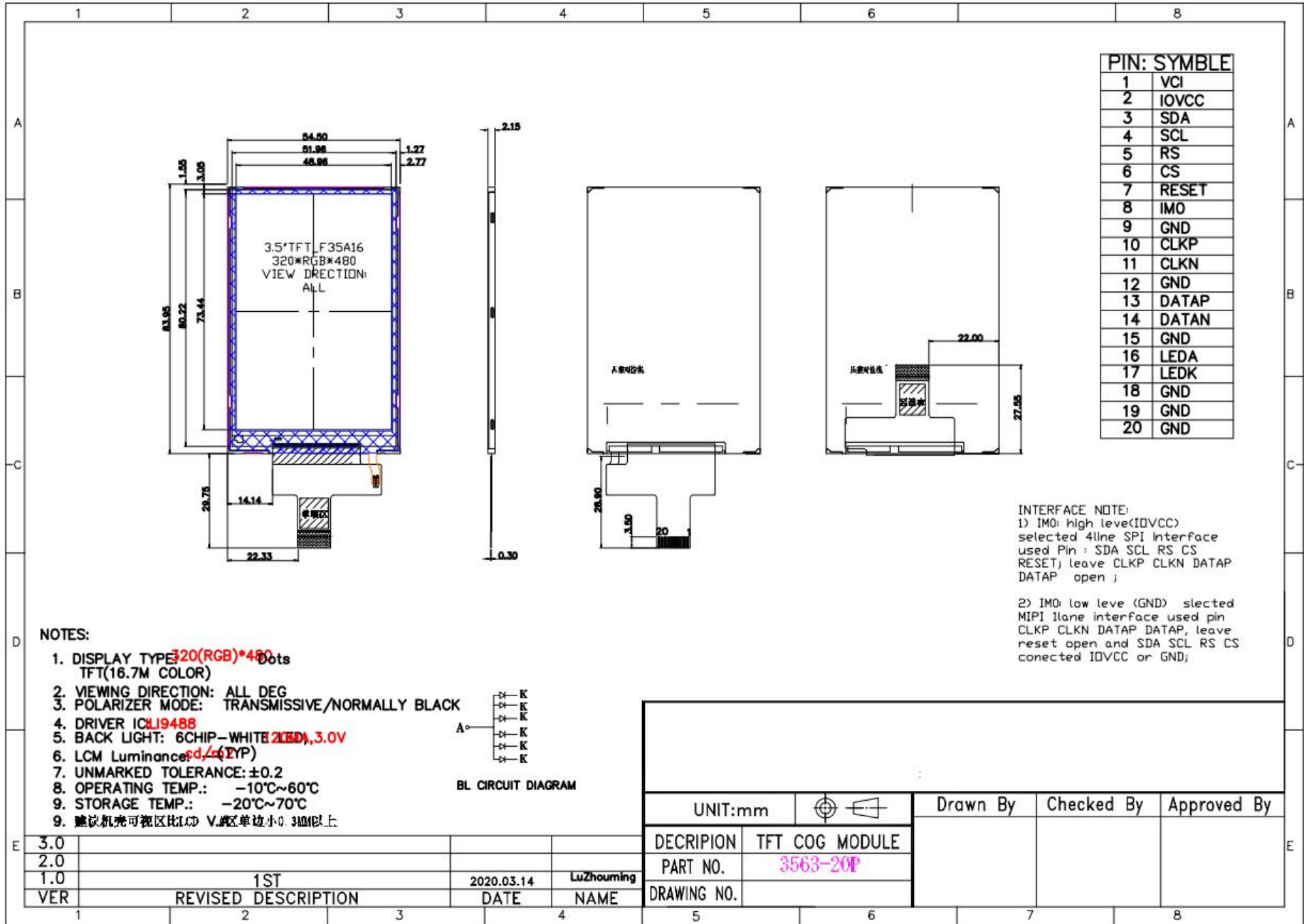
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1. GENERAL INFORMATION 主要特征描述

Item of general information 项目	Contents 内容	Unit 单位
LCD Type 液晶显示类型	TFT/16.7M COLOR	/
Recommended Viewing Direction 模块推荐使用方向	ALL O'Clock	
Module area (W × H×T) 模块外围尺寸 (宽× 高×厚)	54.5 (W)x83.95(H)x2.15(T)	mm
Number of Dots 点阵	320RGB*480 DOT	dots
Pixel pitch (W × H) 像素大小(宽× 高)	0.153 (H) x 0.153 (V)	mm
Driver IC 驱动 IC	ILI9488	/
Interface Type 接口类型	MCU(8080-16bit) or 4WSPI	/
Backlight Type 背光类型	6 LEDs White LED	/

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外形尺寸 DIMENSIONS 2. EXTERNAL





3. ABSOLUTE MAXIMUM RATINGS 极限参数

Parameter of absolute maximum ratings 参数	Symbol 符号	Min 最小值	Max 最大值	Unit 单位
Operating temperature 操作温度	T _{OP}	-10	60	°C
Storage temperature 储存温度	T _{ST}	-20	70	°C

4. ELECTRICAL CHARACTERISTICS 模块电气特性

4.1. DC Characteristics 直流特性

Parameter of DC characteristics 参数	Symbol 符号	Min 最小值	Typ 典型值	Max 最大值	Unit 单位
Supply voltage for logic 逻辑电压	VCC	2.3	2.8		V
I/O power supply 接口电压	IOVCC	1.65	2.8		V
Input voltage 'H' level 输入高电平	V _{IH}	0.8*VDDI		VDDI	V
Input voltage 'L' level 输入低电平	V _{IL}	0.0		0.2*VDDI	V
Output voltage 'H' level 输出高电平	V _{OH}	0.8*VDDI		VDDI	V
Output voltage 'L' level 输出低电平	V _{OL}	0.0		0.2*VDDI	V

4.2. Backlight Characteristics 背光电气特性

Item of backlight characteristics 项目	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward voltage 正向电压	V _f		3.0		v	
Number of LED LED 数量	6LEDs					

Using condition: constant current driving method I_f=120mA(+/-10%).



5. INTERFACE DESCRIPTION 接口定义描述

Pin No.	Symbol	Function												
1	VCC	A supply voltage to the analog circuit. Connect to an external power supply of 2.5 ~ 3.3V.												
2	IOVCC	A supply voltage to the digital circuit. Connect to an external power supply of 1.65 ~ 3.3V.												
3	SDA	serial data input/output bi-direction pin												
4	SCL	Serial Clock for 4-line SPI												
5	RS	Data/Command Selection pin for 4-line SPI												
6	CS	Chip select input signal												
7	RST	Initialize the chip with a low input. Be sure to execute a power-on reset after supplying power.												
8	IM0	<table border="1"> <thead> <tr> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>Interface</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>1</td> <td>0</td> <td>MIPI DSI</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> <td>MIPI-DBI Type C Option 3 (4-line SPI)</td> </tr> </tbody> </table>	IM2	IM1	IM0	Interface	1	1	0	MIPI DSI	1	1	1	MIPI-DBI Type C Option 3 (4-line SPI)
IM2	IM1	IM0	Interface											
1	1	0	MIPI DSI											
1	1	1	MIPI-DBI Type C Option 3 (4-line SPI)											
9	GND	Ground for the internal logic and the analog												
10	MIPI_CLKP	Positive polarity of low voltage differential clock signal Leave the pin open when not in use.												
11	MIPI_CLKN	Negative polarity of low voltage differential clock signal Leave the pin open when not in use.												
12	GND	Ground for the internal logic and the analog												
13	MIPI_DATAP	Positive polarity of low voltage differential data signal Leave the pin open when not in use.												
14	MIPI_DATAN	Negative polarity of low voltage differential data signal Leave the pin open when not in use.												
15	GND	Ground for the internal logic and the analog												
16	LEDA	LED back light (Anode) 3.0V @80~120 mA												
17	LEDK	LED back light (Cathode)												
18	GND	Ground for the internal logic and the analog												
19	GND													
20	GND													



6. ELECTRO-OPTICAL CHARACTERISTICS 光电参数

Item of electro-optical characteristics 项目	Symbol 符号	Condition 条件	Min 最小值	Typ 典型值	Max 最大值	Unit 单位	Remark 注释
Contrast ratio 对比度	CR	$\theta=\psi=0^{\circ}$ $\theta=25^{\circ}$ $I_f=120\text{mA}$		1000	-	-	Note 1
Surface Luminance 表面亮度	Lv		200	260	-	Cd/m^2	Note 2
Luminance uniformity 均匀度	δ_{WHITE}		80	-	-	%	Note 3
Response time 响应时间	Tr+Tf		-	25	-	ms	Note 4
Viewing angle range 视角范围	θ (CR \geq 10)	3o'clock		80	-	degree	Note 5
		12o'clock		80	-		
		9o'clock		80	-		
		6o'clock		80	-		
Module Chromaticity CIE (x, y)	White	x	-	0.308	-	-	Note6
		y	-	0.328	-		
	Red	x	-	0.608	-		
		y	-	0.330	-		
	Green	x	-	0.302	-		
		y	-	0.565	-		
	Blue	x	-	0.144	-		
		y	-	0.111	-		
NTSC Ratio	S	-	-	55.6%	-	-	Note7

Note1. Contrast Ratio (CR) is defined mathematically by the following formula. For more information see FIG 1.:

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

备注 1. 对比度是由以下公式计算所得。详见 FIG 1.。

$$\text{对比度} = \frac{\text{显示白色画面时平均表面亮度(P1,P2,P3,P4,P5)}}{\text{显示黑色画面时平均表面亮度(P1,P2,P3,P4, P5)}}$$

Note2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 1.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

备注 2. 表面亮度是在显示白色画面时，测试的亮度值，详见 FIG 1.。

Lv = 平均的表面亮度(P1, P2, P3, P4, P5)

Note3. The uniformity in surface luminance (δ WHITE) is determined by measuring luminance at each test position 1 through 5, and then dividing the maximum luminance of 5 points luminance by minimum luminance of 5 points luminance. For more information see FIG 1.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3,P4,P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3,P4,P5)}}$$

备注 3. 均匀度是在显示白色画面时，测试 P1 到 P5 的亮度，然后再用 5 个点亮度的最小值除以最大值。

详见 FIG 1.。

$$\text{均匀度} = \frac{\text{表面亮度最小值(P1, P2, P3, P4, P5)}}{\text{表面亮度最大值(P1, P2, P3, P4, P5)}}$$

Note4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 2..

备注 4. 响应时间是 Tr(上升时间)与 Tf(下降时间)的和; Tr 指显示黑色画面转为显示白色画面需要时间, Tf 指显示白色画面转为显示黑色画面需要时间。详见 FIG 2.。

Note5. CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 1.

备注 5. CIE(x,y) 色坐标测试点为显示屏中心点 P5。详见 FIG 1.

Note6. Viewing angle is the angle at which the contrast ratio is greater than 2. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

备注 6. 视角指对比度大于等于 2 时的可视范围，对 TFT 屏，则是对比度大于等于 10 的可视范围。视角由横轴（x 轴），竖轴（y 轴）同 Z 轴(垂直于 LCD 表面)之间的夹角来定义。详见 FIG 3.

Note7: NTSC ratio: For more information see FIG 4.

$$\text{NTSC ratio} = \frac{\text{Area of RGB triangle}}{\text{Area of NTSC triangle}}$$

备注 7. 色域比： 详见 FIG 4

$$\text{NTSC ratio} = \frac{\text{RGB 三色三角形面积}}{\text{标准 NTSC 三角形面积}}$$

Note8. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

备注 8. 视角和响应时间，测试数据基于 Autronic-Melchers's ConoScope. 系列。而对比度，表面亮度，均匀度，CIE 坐标，测试数据基于 BM-7 photo detector.

Note9. For TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle 备注 9. TFT 全透产品,在视角方向会发生灰度反转

FIG.1. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x,y) chromaticity 对比度, 表面亮度, 均匀度, CIE 坐标测试方法

A : 5 mm
 B : 5 mm
 H,V : Active Area
 Light spot size =5mm, 500mm distance from the LCD surface to detector lens
 measurement instrument is luminance meter BM-7

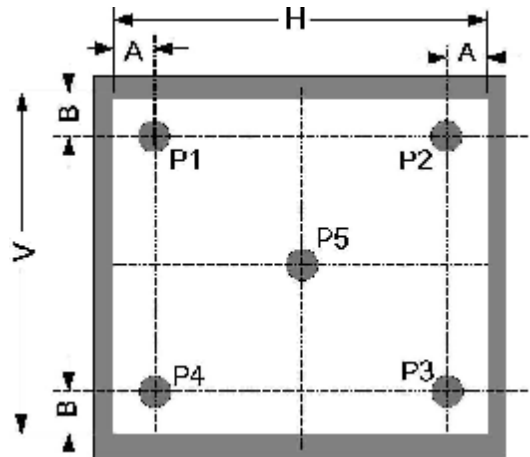


FIG.2. The definition of Response Time 响应时间定义

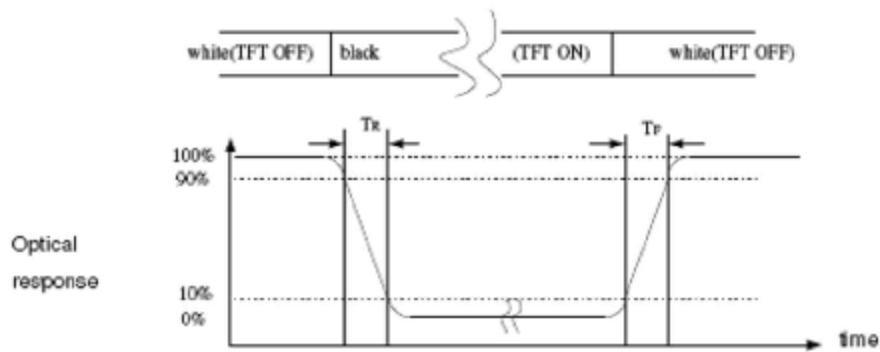


FIG.3. The definition of viewing angle 视角定义

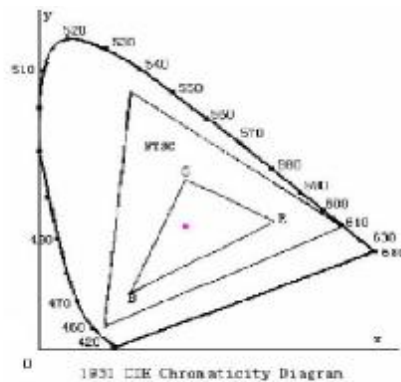
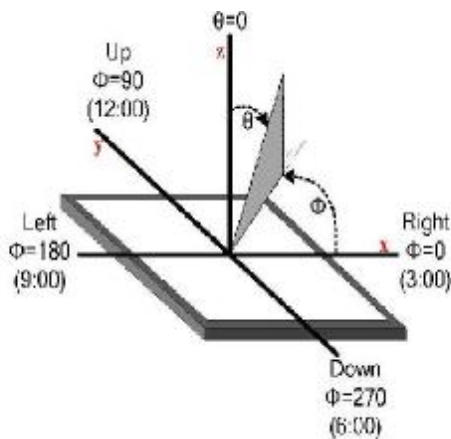


Fig.4. 1931 CIE chromaticity diagram

7. RELIABILITY TEST CONDITIONS 可靠性试验条件

No. 序号	Test Item 试验项目	Test Condition 试验条件	Inspection after test 判断标准
1	High Temperature Storage 高温存放	70±2°C/48 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 试验结束后, 已测试的 LCD 样品必须在室内正常温湿度环境下放置 2~4 个小时以上才能进行功能和外观检查, 样品不允许有以下缺陷: 1.Air bubble in the LCD; 模块中有气泡; 2.Sealleak; 封口松脱; 3.Non-display; 不显示; 4.missing segments; 漏笔 5.Glass crack; 玻璃破碎; 6.Current Idd is twice higher than initial value. 电流 Idd 大于初时值的 2 倍 7, the surface shall be free from damage. 表面无损伤. 8. The electrical characteristics requirements shall be satisfied. 需要满足模块电气性能。
2	Low Temperature Storage 低温存放	-20±2°C/48 hours	
3	High Temperature Operating 高温操作	60±2°C/48hours	
4	Low Temperature Operating 低温操作	-10±2°C/48 hours	
5	Temperature Cycle 冷热循环	-10±2°C (30min.) ~25(5min.)~60(30min.) ± °C 2 ~25(5min.) × 2 cycles	
6	Damp Proof Test 防潮试验		
7	Vibration Test 振荡试验	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 0.5hours (Packing condition)	
8	Dropping test 跌落试验	Drop to the ground from 1.0m height, one time, every side of carton. (Packing condition)	
9	ESD test 静电试验	Voltage:±4KV R: 330Ω C: 150pF Air discharge, 10time	

Remark: 注意:

- The test samples should be applied to only one test item. 每个被测试的模块只能用于其中的一个测试项目。
- Sample size for each test item is 5~10pcs. 每个测试项目的样品数量为 5~10 片。
- For Damp Proof Test, Pure water(Resistance>10MΩ) should be used. 对于防潮试验, 试验箱的用水必须是电阻大于 10M 欧姆的纯水。
- In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part. 如果由静电引起产品故障,当放置一段时间后能够恢复正常, 则不视为产品缺陷。
- EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has. 带 EL 片的可靠性测试在高温高湿条件下, 荧光粉会发生自然化学反应而产生黑点或瑕疵, 因此不在高温高湿条件测试范围内。
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic. 故障判断标准:基本规格,电气特性,机械特性,光电特性

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